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## \*BIBDATASHEET\*

CONFIRMATION NO. 1468

Bib Data Sheet

SERIAL NUMBER 10/677,012	FILING DATE 10/02/2003  RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 67161-112
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## APPLICANTS

Yasunobu Nakase, Hyogo, JAPAN;

\*\* CONTINUING DATA

\*\* FOREIGN APPLICATIONS

JAPAN 2003-002366(P), 01/08/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 01/03/2004

Foreign Priority claimed

☒ yes ☐ no

35 USC 119 (a-d) conditions met

☒ yes ☐ no☒ Met after Allowance

Verified and Acknowledged

Examiner's Signature

Initials

STATE OR

COUNTRY

JAPAN

SHEETS

DRAWING

18

TOTAL

CLAIMS

9

INDEPENDENT

CLAIMS

1

## ADDRESS

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## TITLE

Semiconductor memory device having potential amplitude of global bit line pair restricted to partial swing

FILING FEE

RECEIVED  
770

FEES: Authority has been given in Paper

No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT

No. \_\_\_\_\_ for following:

☐ All Fees☐ 1.16 Fees ( Filing )☐ 1.17 Fees ( Processing Ext. of time )☐ 1.18 Fees ( Issue )☐ Other \_\_\_\_\_☐ Credit